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SPECIFICATION

Product Model: PV07006LZR40E(Rev.V1)

Designed by	R&D Checked by	Quality Department by	Approved by

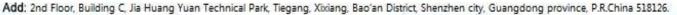
Approval by Customer:

Ok

NG, Problem survey

Approved By

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Revision Record

REV NO.	REV DATE	CONTENTS	Note
V0	2016 11 08	NEW ISSUE	
V1	2017 05 13	Increases indicate that	
V2	2020 01 06	Modify VA and Brightness	

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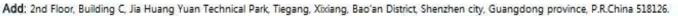










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1. Scope

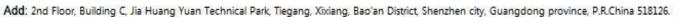
This specification defines general provisions as well as inspection standards for TFT module supplied by Kingtech Group Co.,Ltd.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

2. General Information

TITEM	STANDARD VALUES	UNITS
LCD type	7.0"TFT	
Dot arrangement	1024(RGB)×600	dots
Color filter array	RGB vertical stripe	
Display mode	Normally White , Transmissive	-
Gray Scale Inversion Direction	6 o'clock	
Eyes Viewing Direction	12 o'clock	
Module size	164.90(W)×100.00(H)×5.5(T)	mm
Active area	154.21(W)×85.92(H)	mm
Dot pitch	0. 1506(W)×0. 1432(H)	mm
Interface	LVDS	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	30White LED	
Weight	TBD	g

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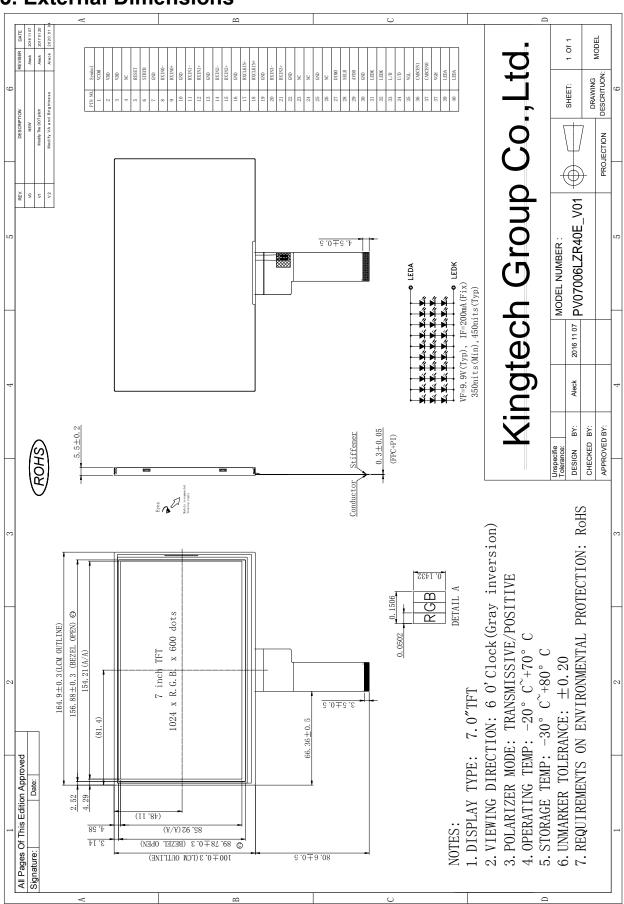


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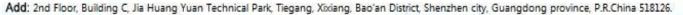
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3. External Dimensions



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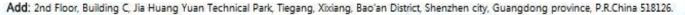
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4. Interface Description

PIN PIN NAME 1 VCOM Common voltage 2 VDD Power for Digital Circuit 3 VDD Power for Digital Circuit 4 NC No connection 5 RESET Global reset pin Standby mode, Normally pulled high STBYB= "1", normal operation STBYB= "0", timing controller, source Driver will turn off, all output are High-z 7 GND Power ground 8 RXINOLVDS differential data pair	
2 VDD Power for Digital Circuit 3 VDD Power for Digital Circuit 4 NC No connection 5 RESET Global reset pin Standby mode, Normally pulled high STBYB= "1", normal operation STBYB= "0", timing controller, source Driver will turn off, all output are High-z 7 GND Power ground	
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The process of the pr	
8 PYINO I VDS differential data pair	
O INTINU LVD3 unierential data pan	
9 RXIN0+ +LVDS differential data pair	
10 GND Power ground	
11 RXIN1LVDS differential data pair	
12 RXIN1+ +LVDS differential data pair	
13 GND Power ground	
14 RXIN2LVDS differential data pair	
15 RXIN2+ +LVDS differential data pair	
16 GND Power ground	
17 RXCLKINLVDS differential clock pair	
18 RXCLKIN+ +LVDS differential clock pair	
19 GND Power ground	
20 RXIN3LVDS differential data pair	
21 RXIN3+ +LVDS differential data pair	
22 GND Power ground	
23 NC No connection	
24 NC No connection	
25 GND Power ground	
26 NC No connection	
Backlight CABC controller signal output 27 DIMO =L Turn off external backlight controller DIMO=H Logical control signal to turn on external backlight control	ller
28 SELB 6bit/8bit mode select(L=8-bit;H=6-bit)	
29 AVDD Power for Analog Circuit	
30 GND Power ground	
31 LEDK LED backlight (Cathode).	
32 LEDK LED backlight (Cathode).	
33 L/R Horizontal inversion(L=shift left; H=shift right)	
34 U/D Vertical inversion(L=Up → Down ; H= Down →Up)	

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36	CABCEN1	CABC H/W enable Note:1
37	CABCEN0	CABC H/W enable Note:1
38	VGH	Gate ON Voltage
39	LEDA	LED backlight (Anode).
40	LEDA	LED backlight (Anode).

Note:1

CABCEN1	CABCEN0	DESCRIPTION
L	L	CABC OFF
L	Η	User interface Image
Н	L	Still Picture
Н	Н	Moving Image

5. Electrical specification

5. Electrical Specification								
Item	Symbol	Min.	Тур.	Max.	Unit	Remark		
Input signal Voltage	VCOM	3.3	3.8	4.3	V	-		
Logic Supply Voltage	VDD	2.5	3.3	3.6	V			
Analog Supply Voltage	AVDD	8	11	13.5	٧			
Low Supply Voltage	VGL	-7.1	-6.8	-6.5	V	-		
High Supply Voltage	VGH	19.7	20	20.3	V			
Output High Voltage	VIH	0.7XVDD	-	VDD	٧	-		
Output Low Voltage	VIL	0	-	0.3xVDD	V	-		

6. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	DVDD	-0.5	5	V
Analog Supply Voltage	VDD	-0.5	15	V
High Supply Voltage	VGH	-0.3	42	V
Low Supply Voltage	VGL	-20	0.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C

7. Current Consumption

Item	Symbol	Values			Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Offic	Remark	
High Supply for Current	IGH	-	0.2	2	mA	VGH=20V	
Low Supply for Current	IGL	-	0.2	2	mA	VGL=-6.8V	
Logic Supply for Current	IVDD	-	38	80	mA	VDD=3.3V	
Analog Supply for Current	IAVDD	-	20	30	mA	AVDD=11V	

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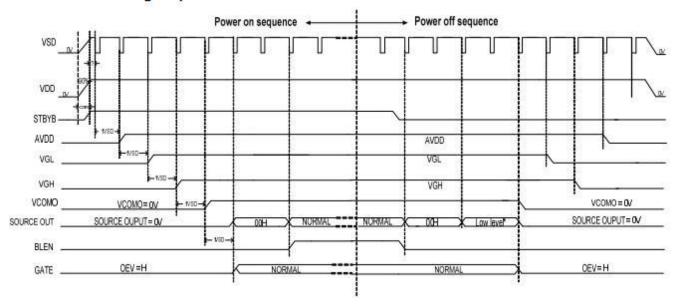


8. Timing Characteristics

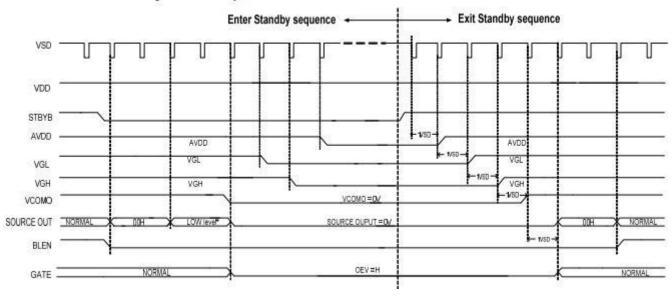
8.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence:



Enter and Exit Standby Mode Sequence:



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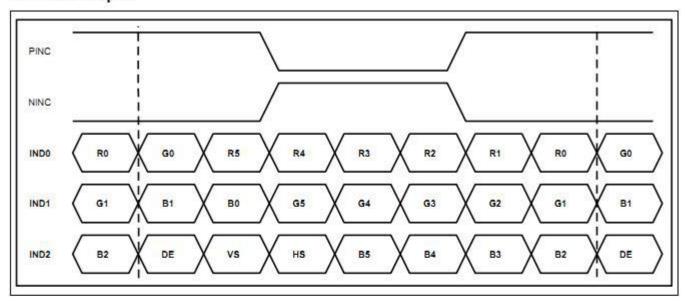
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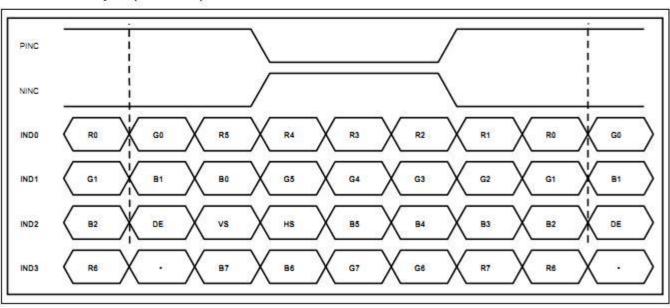


8.2 Data Input Format for LVDS

6bit LVDS input



8-bit LVDS input (HSD='L')



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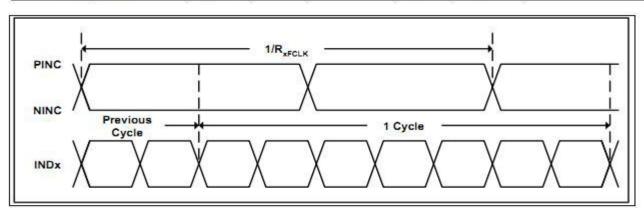
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8.3 AC Electrical Characteristics

LVDS mode

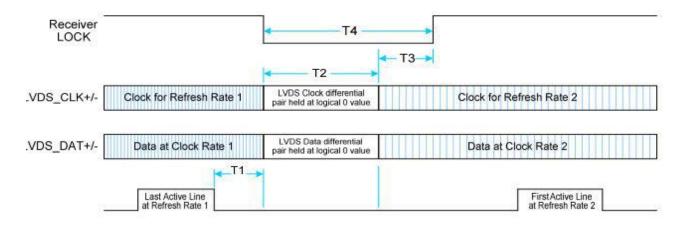
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Clock frequency	RxFCLK	20	. Introduction	71	MHz	
Input data skew margin	T _{RSKM}	500			pS	V _{ID} = 400mV R _{XVCM} = 1.2V R _{XFCLK} = 71 MHz
Clock high time	TLVCH		4/(7* R _{xFCLK})		ns	
Clock low time	TLVCL		3/(7* R _{xFCLK})		ns	
PLL wake-up time	TenPLL			150	uS	



8.4 Timing

SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



- T1 Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)
- T2 Max delay for clock to transition to new frequency: 100us
- T3 Max receiver lock delay from stable clock: Display specific (TBD)
- T4 Max period during which panel maintains display (T2+T3): Display specific (TBD)

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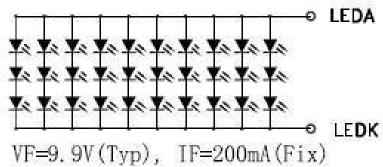
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9. Backlight Characteristic



Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	8.1	9.9	10.5	V	If=200mA
Supply Current	If		200	-	mA	-
Luminous Intensity for LCM	-	350	450	-	cd/m ²	If=200mA
Uniformity for LCM	-	80	-	-	%	If=200mA
Life Time	-	-	50000	-	Hr	If=200mA
Backlight Color	White					

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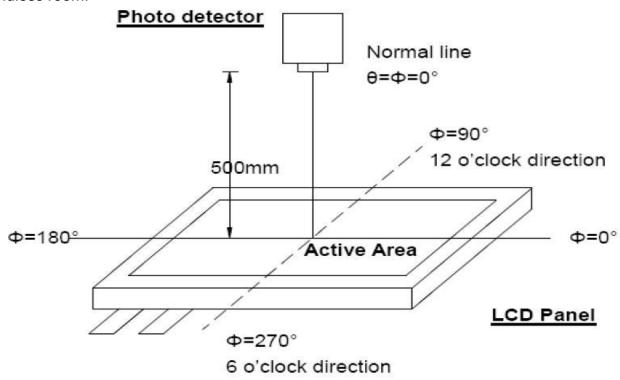
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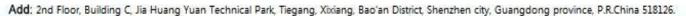
10. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note
Viewing Angle	Horizontal	θL	-	75	-	degree	(1),(2),(6)
		θR	-	75	-		
(CR>10)	Vertical	θт	-	70	-		
		θв	-	75	-		
Contrast Ratio	Center		500	800	-	-	(1),(3),(6)
Dognopo Timo	Rising		-	10	20	ma	(1) (4) (6)
Response Time	Falling		-	15	30 ms		(1),(4),(6)
	Red x			TBD		-	
	Red y			TBD		-	
	Green x			TBD		-	
CF Color	Green y		Тур. ТВІ	TBD	Тур.	-	(4) (6)
Chromaticity (CIE1931)	Blue x Blue y White x		-0.05	TBD	+0.05	-	(1), (6)
				TBD		-	
				TBD		-	
	White y			TBD		-	
Transmittance	Θ= 0°			3.37		%	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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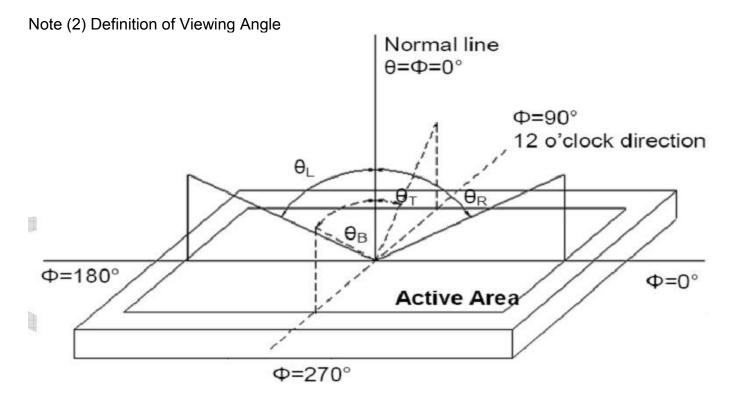


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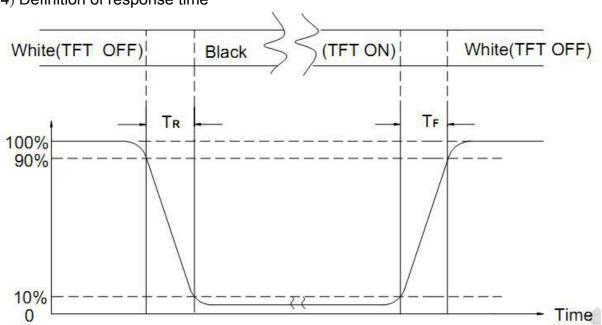


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

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11. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
1)	High Temperature Storage	80°C±2°C×96Hours	
2	Low Temperature Storage	-30°C±2°C×96Hours	
3	High Temperature Operating	70°C±2°C×96Hours	
4	Low Temperature Operating	-20°C±2°C×120Hours	Inspection after 2~4hours storage at room temperature, the samples
(5)	Temperature Cycle(Storage)	-20°C \Longrightarrow 25°C \Longrightarrow 70°C (30min) (30min) 1cycle Total 10cycle	should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments.
6	Damp Proof Test (Storage)	50°C±5°C×90%RH×96Hours	5, Glass crack. 6, Current IDD is twice
7	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5M X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
8	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	Silan de Sausneu.
9	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Ai r Mode,10times	

REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3,For Damp Proof Test, Pure water(Resistance $> 10M\Omega$)should be used.
- 4,In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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12. Inspection Standard

12.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

12.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 °C TO 40 °C , AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

12.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL II SINGLE PLAN.

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %
TOTAL	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

12.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

12.2. CHECKING CONDITION

- 12.2.1. CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- 12.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

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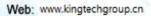
12.3. INSPECTION PLAN:

CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	OUTSIDE AND INSIDE PACKAGE	"MODEL NO.", "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED QUANTITY SHORT OR OVERREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
APPEARANCE	6. BLEMISH - BLACK SPOT - WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	7. BLEMISH - BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST: VOP: CHROMATICITY ETC.)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT-LINE - CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT- WRONG PATTERN DISPLAY	NO DISPLAY - WRONG PATTERN DISPLAY - CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor

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12.4 STANDARD OF VISUAL INSPECTION

NO.	CLASS	ITEM	JUDGEMENT			
		1	(A) ROUND TYPE: unit : mm.			
12.4.1 MINOR		DIAMETER (mm.) A	ACCEPTABLE Q'TY			
		Φ ≤ 0.1	DISREGARD			
	BLACK AND WHITE SPOT FOREIGN MATERIEL DUST IN THE CELL BLEMISH	0.1 < Φ ≤ 0.25	3 (Distance>5mm)			
		0.25 < Ф	0			
		NOTE: Φ=(LENGTH+WIDTH)/ (B) LINEAR TYPE:	/2 unit : mm.			
		SCRATCH	LENGTH WIDTH	ACCEPTABLE Q'TY		
			W ≤	0.03 DISREGARD		
			L ≤ 5.0 0.03 < W ≤	0.07 3 (Distance>5mm)		
			0.07 < W	FOLLOW ROUND TYPE		
				unit : mm.		
			DIAMETER	ACCEPTABLE Q'TY		
		BUBBLE IN POLARIZER DENT ON POLARIZER	Φ ≤ 0.2	DISREGARD		
12.4.2	MINOR		0.2 < Φ ≤ 0.5	2 (Distance>5mm)		
			0.5 < Ф	0		
		Dot Defect	Items Bright dot	ACC. Q'TY N≦ 4		
		Dot Delect	Dark dot	N ≤ 4 N ≤ 4		
12 4.3	MINOR		Note 3: Dark dot: Dots appear dar	size of a defective dot over ed as one defective dot. right and unchanged in size playing under black pattern.		

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NO.	CLASS	ITEM	JUDGEMEN	T
12.4.4	MINOR	LCD GLASS CHIPPING	F. I	Y > S Reject
12.4.5	MINOR	LCD GLASS CHIPPING	S. T.	X or Y > S Reject
12.4.6	MAJOR	LCD GLASS GLASS CRACK	Y Y	Y > (1/2) T Reject
12.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	A + B	1. a> L/3 , A>1.5mm. Reject 2. B : ACCORDING TO DIMENSION
12.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	T T	$\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject
12.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	T Y	Y > (1/3) T Reject
12.4.10	MINOR	LCD GLASS CHIPPING	Z	Y > T Reject

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13. Handling Precautions

13.1 Mounting method

The LCD panel of KINGTECHTFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

13.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

13.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

13.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

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13.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

13.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

14. Precaution for Use

14.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

14.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to KINGTECHTFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

15. Packing Method

TBD

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